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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Joseph Stoddard et al.

§ Group Art Unit:

Serial No.: 10/025,665

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Filed: December 19, 2001

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Examiner:

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For: Method And Apparatus For Adding
Inductance To Printed Circuits

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Atty. Dkt. No.: MAX.0002US

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Commissioner for Patents
Washington DC 20231

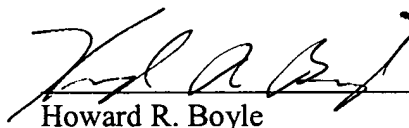
ASSERTION OF SMALL ENTITY STATUS
(37 C.F.R. § 1.27(c)(1))

Dear Sir:

Applicant hereby states that applicant is a small entity and that status as a small entity is asserted for this application.

Respectfully submitted,

Date: 2/21/02

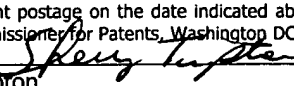


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PATENT TRADEMARK OFFICE

Date of Deposit: 2/21/02
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, Washington DC 20231.

Sherry Tipton